



OV-7604-C7

Low Power Clock Oscillator 32.768 kHz

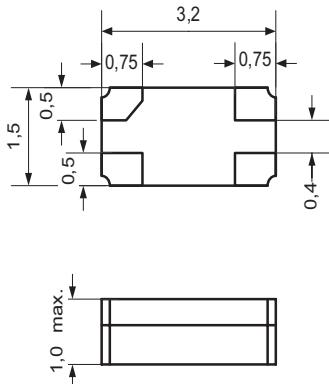


100% leadfree, RoHS - compliant

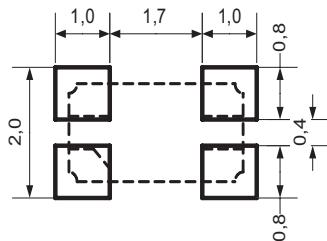
Xtal integrated solution
Ultra miniature SMT ceramic pack.
Ind. and Ext. temperature ranges
Package height 1.0 mm
Very low power consumption
Very tight frequency tolerance
Low aging
High shock and vibration resistance

DIMENSIONS

Package:



Recommended Solder Pad:



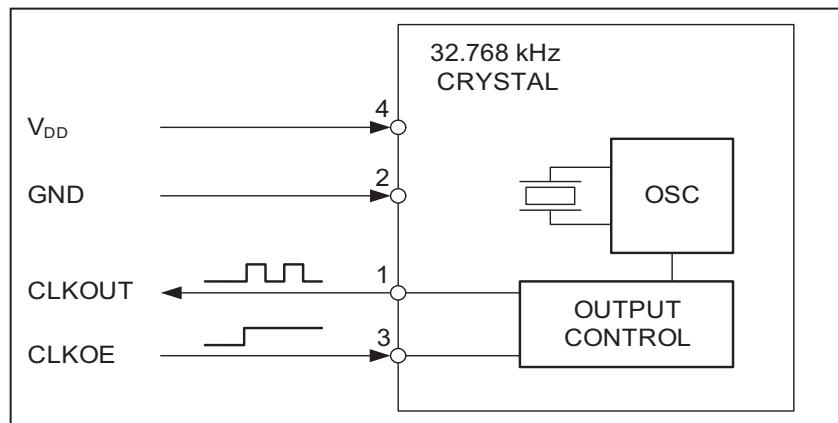
All dimensions in mm typical

DESCRIPTION:

This very small SMD ceramic package has been specially designed to combine the 32.768 kHz crystal unit with the CMOS-based oscillator circuit. Ultra Low Power consumption, as low as 300 nA, are made possible through 32.768 kHz Tuning-Fork-Crystals and sophisticated CMOS-circuitry.

For pick-and-place equipment, the parts are available in 12 mm tape:
 7" (178 mm) reel with 1'000 oscillators
 7" (178 mm) reel with 3'000 oscillators

BLOCK DIAGRAM:



PIN CONNECTIONS TOP VIEW:

Pin	Connection
1	CLKOUT Frequency output
2	GND Ground
3	CLKOE CLK output enable
4	V _{DD} Power Supply Voltage

Production Date Code
 #4 #3
 M 908 A 4
 7604
 #1 #2
 Pin 1 Index Part Designation

**ELECTRICAL CHARACTERISTICS
AT 25°C:**

Output Frequency	F	32.768	kHz
Frequency tolerance	¹⁾ ΔF/F	+/-20	ppm
Supply voltage	V _{DD}	1.2 – 5.5	V
Current consumption (typ./max.)	I _{DD}	0.3 / 0.5	μA
Duty cycle (min./max.)		40 / 60	%
Output voltage (high)	³⁾ V _{OH}	V _{DD} – 0.4	V
Output voltage (low)	⁴⁾ V _{OL}	Gnd + 0.4	V
Output Load	HCMOS	15	pF
Output Current	I _{OH}	1	mA
	I _{OL}	-1	mA
Output rise time (max.)	⁵⁾ t _r	70	ns
Output fall time (max.)	⁵⁾ t _f	70	ns
Start up time (max.)	t _{start}	0.5	s
CLK Output Enable (High)	V _{IH}	80% V _{DD} min.	V
CLK Output Disable (Low)	V _{IL}	20% V _{DD} max.	V
Voltage coefficient (max.)		+/-1.5	ppm/V
Aging first year max. @ 25°C	ΔF/F	+/-3	ppm
Turnover temperature (typ.)	T ₀	25 +/- 5	°C
Frequency vs. temperature	ΔF/F ₀	-0.035 ppm/ ^o C ² (T - T ₀) ² +/-10%	ppm

1) Tighter tolerances on request.

4) I_{OL} = 1.0 mA.

2) V_{DD} = 3.0 V OE.

5) C_L = 10 pF, 10% to 90%.

3) I_{OH} = -1.0 mA.

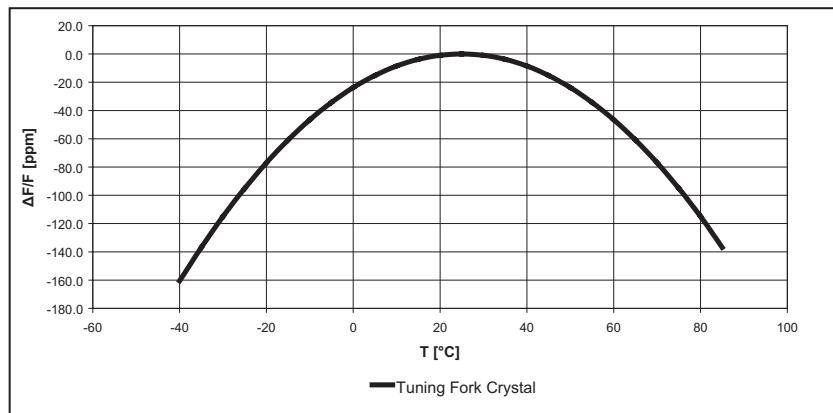
**ENVIRONMENTAL
CHARACTERISTICS:**

		Conditions	Max. Dev.
Storage temp. range		-55 to +125°C	
TA Operating temperature range		-40 to +85°C	
TB Extended oper. temp. range		-40 to +125°C	
Shock resistance	ΔF/F	5000 g, 0.3 ms, ½ sine	+/-5 ppm
Vibration resistance	ΔF/F	20 g / 10–2000 Hz	+/-5 ppm

**TERMINATIONS AND
PROCESSING:**

Package-Type	Termination	Processing
Ceramic	For SMD mounting Au plated pads	Reflow soldering 260°C / 20 s max.

**FREQUENCY TEMPERATURE
CHARACTERISTICS:**



All specifications subject to change without notice.



Micro Crystal AG
Mühlestrasse 14
CH-2540 Grenchen
Switzerland

Tel. +41 32 655 82 82
Fax +41 32 655 82 83
sales@microcrystal.com
www.microcrystal.com